# 4V Drive Nch MOS FET RSQ045N03

#### Structure

Silicon N-channel MOS FET

#### ● Features

- 1) Low On-resistance.
- 2) Space saving, small surface mount package (TSMT6).
- 3) Low voltage drive (4V drive).

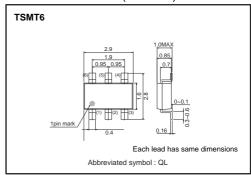
## Applications

Switching

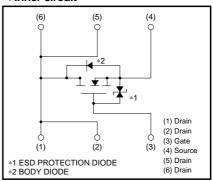
### Packaging specifications

	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	3000
RSQ045N0	0	

## ●External dimensions (Unit : mm)



#### Inner circuit



## ● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		$V_{\text{DSS}}$	30	V
Gate-source voltage		V <sub>GSS</sub>	20	V
Drain current	Continuous	$I_{D}$	±4.5	Α
Diam current	Pulsed	I <sub>DP</sub> *1	±18	Α
Source current	Continuous	Is	1.0	Α
(Body diode)	Pulsed	I <sub>SP</sub> *1	18	Α
Total power dissipation		P <sub>D</sub> *2	1.25	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

<sup>\*1</sup> Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board

### ●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	100	°C/W

<sup>\*</sup> Mounted on a ceramic board

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	10	μА	Vgs=20V, Vps=0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	30	_	_	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	_	1	μΑ	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	_	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance		-	27	38	mΩ	I <sub>D</sub> = 4.5A, V <sub>GS</sub> = 10V
	R <sub>DS (on)</sub> *	_	36	51	mΩ	I <sub>D</sub> = 4.5A, V <sub>GS</sub> = 4.5V
		-	40	56	mΩ	I <sub>D</sub> = 4.5A, V <sub>GS</sub> = 4V
Forward transfer admittance	Y <sub>fs</sub>   *	3.5	-	_	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.5A
Input capacitance	Ciss	-	520	_	pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	150	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	95	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	12	_	ns	V <sub>DD</sub> ≒ 15V
Rise time	tr *	_	19	_	ns	I <sub>D</sub> = 2.25A V <sub>G</sub> s= 10V
Turn-off delay time	t <sub>d (off)</sub> *	_	41	_	ns	$R_{L}=6.67\Omega$
Fall time	t <sub>f</sub> *	-	14	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	-	6.8	9.5	nC	V <sub>DD</sub> ≒15V V <sub>GS</sub> =5V
Gate-source charge	Q <sub>gs</sub> *	-	1.6	-	nC	ID= 4.5A
Gate-drain charge	Q <sub>gd</sub> *	_	2.3	_	nC	RL=3.33Ω Rg=10Ω

\*Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	-	1.2	V	I <sub>S</sub> = 1.0A, V <sub>GS</sub> =0V

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